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To:	Mail Stop AF	From:	Kyle Eppele
Location:	U.S. Patent & Trademark	Location:	124-323
Fax:	703-872-9306	Fax:	319-295-8777
Tel:		Tel:	319-295-8280
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Applicant: M. Peterson et al.

Serial No.: 09/931,493

Filed: August 16, 2001

For: System For Phase, Gain and DC Offset Error Correlation For A Quadrature Modulator

Docket No./Attorney: 99CR098/KE

Item: Response comprising of 7 pages

Kyle Eppele
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RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2600**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**First Named
Inventor : Maurice W. Peterson et al.

Group Art Unit: 2634

Appln. No. : 09/931,493

Examiner: E. File

Filed : August 16, 2001

For : SYSTEM FOR PHASE, GAIN AND DC
OFFSET ERROR CORRECTION FOR A
QUADRATURE MODULATOR

Docket No. : 99CR098/KE

RESPONSE AFTER FINAL**Mail Stop AF**

(703) 872-9306

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This is in Response to the Final Office Action mailed on April 1, 2005 in which claims 1-20 were rejected. With this Response After Final, all claims remain unchanged and are presented for reconsideration and allowance in view of the following remarks. Also, remarks presented in the Response filed on February 10, 2005 are incorporated by reference in this Response After Final, but for the sake of brevity are not repeated in their entirety.

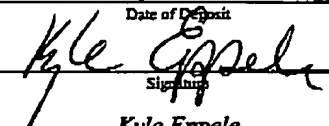
It is noted that in sections 2-6 of the Final Office Action, the Examiner provided specific responses to Applicant's previously presented arguments. The Examiner's effort in addressing Applicant's previously presented arguments is appreciated. It is maintained however that pending claims 1-20 are allowable over the cited references.

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